IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Donald Humlicek

Serial No.:

09/645,949

Filed:

August 25, 2000

For:

Raid Volume For Sequential Use That Needs No Redundancy Pre-

Initialization

Group Art Unit:

2186

Examiner:

Tran, Denise

Atty Docket:

1496.00035 / 00-275

I hereby certify that this correspondence is being deposited with the U.S. Postal Service as First Class Mail in an envelope addressed to: Commissioner for Patents, P. O. Box 1450, Alexandria, VA 22313-1450, on the date below:

SUBMISSION OF FORMAL DRAWINGS PURSUANT TO 37 C.F.R. §1.85

Official Draftsman

Commissioner for Patents P. O. Box 1450 Alexandria, VA 22313-1450

Sir:

Applicant hereby substitutes the enclosed formal drawings for those presently in the above referenced application.

LSI Logic Corporation 1621 Barber Lane, MS D-106 Milipitas, CA 95035 408-433-7475

Date: 9-17-04

Respectfully submitted,

Sandeep Jaggi

Reg. No. 43,331